

DC5302P/4P/4S/8P/8S-ADC Schematic and Layout

Introduction

The **DC5302P_4P_4S_8P_8S-ADC Schematic & Layout.zip** contains the daughtercard schematic and layout information for the DC530x-ADC customer evaluation board with CS5302P, CS5304P, CS5304S, CS5308P and CS5304P silicon, as well as a customer reference.

Table 1 Board Information

Board Name	DC5302P-ADC	DC5304P-ADC	DC5304S-ADC	DC5308P-ADC	DC5308S-ADC
Board Type	Daughter Card	Daughter Card	Daughter Card	Daughter Card	Daughter Card
Device	CS5302P	CS5304P	CS5304S	CS5308P	CS5308S
No of ADCs	2	4	4	8	8

1 Folder Contents

The contents of the **DC5302P_4P_4S_8P_8S-ADC Schematic & Layout.zip** is detailed in Table 2.

Table 2 DC5302P/4P/4S/8P/8S-ADC Schematic & Layout.zip Contents

PCB Schematics	DC5302P-ADC-SCH-REV-E0.pdf DC5304P_4S-ADC-SCH-REV-E0.pdf DC5308P_8S-ADC-SCH-REV-E0.pdf
PCB Layout	DC530X-DAC-PCB-REV-E0.pdf
PCB Schematic & Layout Guidelines	CS530X Schematic Layout Guidelines.pdf
PCB Layout Gebers	DC530X-DAC-GERBER-REV-E0.zip
PCB Bill of Materials	DC5302P-ADC-BOM-REV-E0.pdf DC5304P_4S-ADC-BOM-REV-E0.pdf DC5308P_8S-ADC-BOM-REV-E0.pdf

2 Revision History

Revision History	
Revision	Changes
1.0 July 2025	• Initial version.
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Contacting Cirrus Logic Support

For all product questions and inquiries, contact a Cirrus Logic Sales Representative.

To find the one nearest you, go to www.cirrus.com.

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